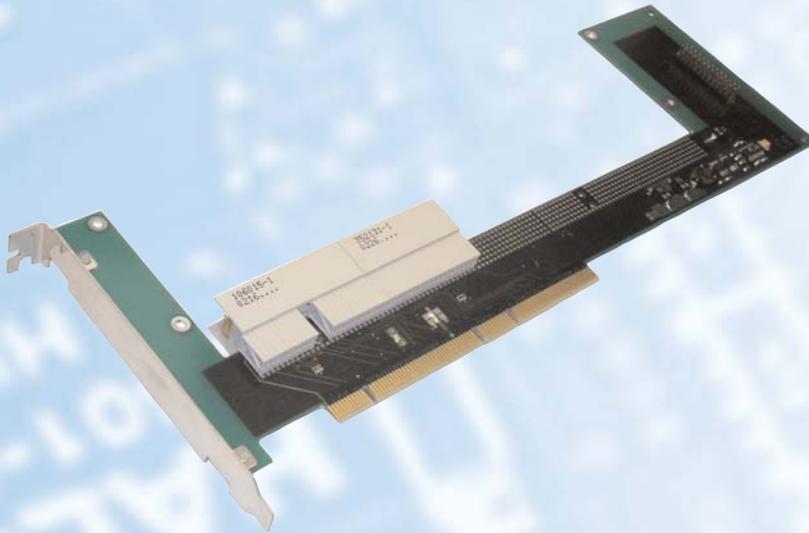


PCI - to - cPCI adapter



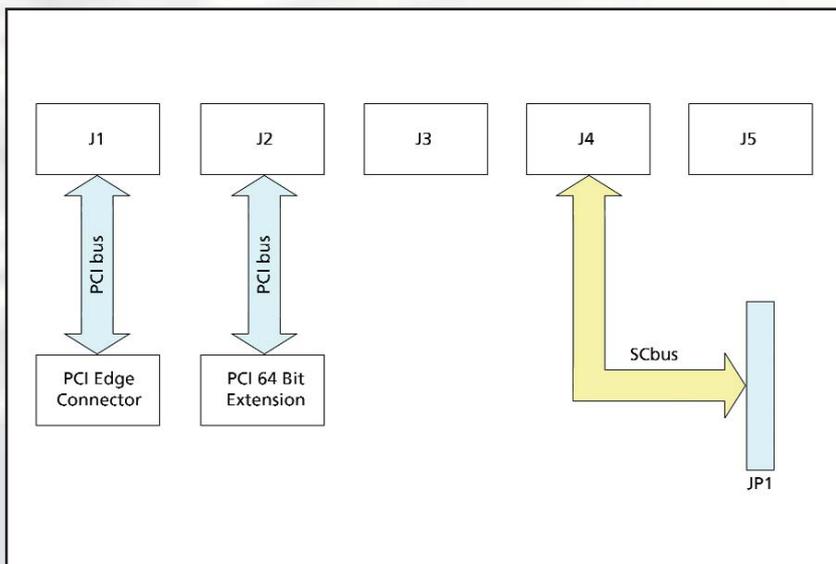
NPCI-CPCI

The NPCI-CPCI adapter is a passive PCI-to-cPCI adapter board providing a low-cost and effective means for development, testing and debugging of CompactPCI boards in PCI based systems.

The NPCI-CPCI allows the user to operate a cPCI board in a standard PC environment, including the H.110 TDM bus, which is available on a male header connector.

Thus the NPCI-CPCI is a cost-saving and efficient tool to establish a complete software development and test environment for cPCI boards.

Technical Data



The NPCI-CPCI adapter is a passive extender board intended for use of cPCI boards in standard PC environments.

The NPCI-CPCI has two connector sets, a PCI edge connector plugging into the motherboard, and a cPCI connector set (J1 - J5) dedicated to receive the CompactPCI board under test.

Connector J4 of the cPCI interface carries the H.110 specific signals as defined within the cPCI specification. The SCbus (SCSA) known in a PC environment consists of signals, which form a subgroup of the signals defined for H.110 bus.

These SCbus signals are routed to a 26-pin male header according to standard SCbus definition for PCs. A flat cable can be used to connect the SCbus connectors of all add-in cards using this bus.

Therefore, the NPCI-CPCI is ideally suited for software development, debugging and testing of CompactPCI cards with or without H.110 bus in a PC chassis.

PCI Interface

33/66MHz, 64-bit, non-active

CompactPCI Interface

33/66MHz, 64-bit, non-active

Power Consumption

Current drawn from +3.3V, +5V, +12V, -12V, V(I/O) : less than 10mA each.
+3.3V, +5V, V(I/O), and GND connected to separate power planes

Environmental

-40°C to +85°C (operating and storage)
Relative Humidity: 5% to 95% (non-condensing)

Standard Compliance

PCI Rev. 2.2

PICMG2.1 R2.0

PICMG 2.5 R1.0

N.A.T.

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